

	L #	Hits	Search Text	DBs
1	L1	539	361/777.ccls.	USP AT; US-P GPU B
2	L2	807	257/786.ccls.	USP AT; US-P GPU B
3	L3	766	2 not 1	USP AT; US-P GPU B
4	L4	571	3 and (pitch density rout\$3 pattern\$2)	USP AT; US-P GPU B
5	L5	195	3 not 4	USP AT; US-P GPU B
6	L6	43	5 and (ball\$1 bump\$1 land\$1 bga lga)	USP AT; US-P GPU B
7	L7	2946	(174/250;260,261,262).c cls.	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
8	L8	2660	7 not (1 4 6)	USP AT; US-P GPU B
9	L9	1783	8 and (pitch density rout\$3 pattern\$2)	USP AT; US-P GPU B
10	L10	833	9 and (ball\$1 bump\$1 land\$1 bga lga)	USP AT; US-P GPU B
11	L11	950	9 not 10	USP AT; US-P GPU B
12	L12	338	11 and (dense display bus busse\$1 computer\$1 processor\$1)	USP AT; US-P GPU B
13	L13	1133	(361/772,773,774).ccls.	USP AT; US-P GPU B
14	L14	1751	(257/773,775,776).ccls.	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
15	L15	661	13 and (pitch density rout\$3 pattern\$2)	USP AT; US-P GPU B
16	L16	1322	14 and (pitch density rout\$3 pattern\$2)	USP AT; US-P GPU B
17	L17	45	13 and dense	USP AT; US-P GPU B
18	L18	119	14 and dense	USP AT; US-P GPU B
19	L19	664	15 17	USP AT; US-P GPU B
20	L20	1327	16 18	USP AT; US-P GPU B
21	L21	425	19 not (1 4 6 10 12)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
22	L22	1174	20 not (1 4 6 10 12)	USP AT; US-P GPU B
23	L23	151	21 and (ball\$1 bump\$1 land\$1 bga lga)	USP AT; US-P GPU B
24	L24	241	22 and (ball\$1 bump\$1 land\$1 bga lga)	USP AT; US-P GPU B
25	L25	381	23 24	USP AT; US-P GPU B
26	L26	3905	1 4 6 10 12 19 20	USP AT; US-P GPU B
27	L27	11	26 and (bus with display\$1)	USP AT; US-P GPU B
28	L28	105	26 and ((memory memories) with bus)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
29	L29	49	26 and (processor\$1 with bus)	USP AT; US-P GPU B
30	L30	41	26 and (microprocessor\$1 with bus)	USP AT; US-P GPU B
31	L31	78	29 30	USP AT; US-P GPU B
32	L32	5	27 and 28 and 31	USP AT; US-P GPU B
33	L33	526	257/691.ccls.	USP AT; US-P GPU B
34	L34	378	33 and (pitch density rout\$3 pattern\$2)	USP AT; US-P GPU B
35	L35	0	34 and (bus with display\$1)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
36	L36	2106 6	bus with display\$1	USP AT; US-P GPU B
37	L37	6703 0	(memory memories) with bus	USP AT; US-P GPU B
38	L38	5158 6	(processor\$1 microprocessor\$1) with bus	USP AT; US-P GPU B
39	L39	9096	36 and 37 and 38	USP AT; US-P GPU B
40	L40	4999	39 and (density dense pitch rout\$3)	USP AT; US-P GPU B
41	L41	2132	40 and (substrate\$1 board\$1 pcb\$1 pwb\$1)	USP AT; US-P GPU B
42	L42	339	41 and (ball\$1 bump\$1 land\$1 bga lga)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
43	L43	337	42 not (1 4 6 10 12 25)	USP AT; US-P GPU B